

FACULTY PROFILE

Name: KAPPALA INDIRA PRIYADARSHINI

Designation: LECTURER

Qualification: M.Tech (pursuing), B.Tech

Date of birth: 18-08-1995



Experience: **Years**
Teaching: **Industrial:** **Research:**

Area of Specialization: Electronics and Communication Engineering

Date of joining: 07-02-2024

Research Interests: VLSI & Embedded Systems

Address for Communication: 1-13-26, Gollagudem 33rd Ward, near Ladies Hostel, Tadepalligudem, West Godavari-534101

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Academic Degree: (From highest)

Degree	School/College	Board/University	Year of Pass
M.Tech	Sri Vasavi Engineering College	JNTUK	Pursuing
B.Tech	Sri Vasavi Engineering College	JNTUK	2016
Intermediate	Sri Chaitanya Junior College	Board of Intermediate Education, A.P	2012
SSC	T.S.M.Z.P.P.High School	Board of Secondary Education Andhrapradesh	2010

Experience particulars: (latest first)

College/University	Designation	Duration

Administration:

Papers Published in referred Journals:

National:

International:

Papers presented in Conferences:

National:

International:

Keynote address/Guest Lectures delivered:

No. of Workshops / Seminars/FDPs Conducted:

No. of Workshops / Seminars/FDPs Participated:

No. of projects guided/guiding:

UG:

PG:

No. of Ph.D's guided/guiding:

Memberships in Professional Societies:

Achievements: (If any)